

AMENDMENT TO THE SPECIFICATION

Please replace the paragraph beginning on page 8, line 18, with the following amended paragraph:

A fixed reference voltage V_{FIXED} is coupled to the current selector 52 and to a first end of a precision resistor R_X residing off of the IC 50. A second end of the precision resistor R_X is coupled to the current selector 52 such that the current selector 52 can measure the fixed reference voltage V_{FIXED} and the voltage V_X at the second end of the precision resistor R_X . The voltage V_X is also coupled to the drains of the first set of binary weighted transistors of each current station 54. The current selector 52 adjusts the current through the first set of binary weighted FETs for each current station ~~[[52]]~~ 54 based on the measured voltage drop across the precision resistor R_X , until a desired first current is achieved. The drains of the second set of binary weighted FETs for each of the current stations ~~[[52]]~~ 54 are coupled to a current source terminal $\text{CS}_{\text{TERMINAL}}$. In this example, the current source terminal $\text{CS}_{\text{TERMINAL}}$ is coupled to a supply voltage V_{SUPPLY} through one or more device(s) 56 on the IC. However, the current source terminal $\text{CS}_{\text{TERMINAL}}$ can be directly coupled to the supply voltage V_{SUPPLY} depending on the particular implementation being employed.